



Tutorials November 4, 2012

<p>08:30 Registration</p> <p>09:15 Opening Remarks <i>Itai Segall, IBM Research – Haifa</i></p> <p>09:30 Tutorial 1: Idiom-based Verification of Highly Concurrent Data Structures Using Temporal Separation Logic <i>Dr. Noam Rinetzky, Tel Aviv University</i></p> <p>11:00 Break</p> <p>11:30 Tutorial 2: Three-Valued Abstraction-Refinement <i>Dr. Sharon Shoham Buchbinder, Academic College of Tel Aviv-Yaffo</i></p>	<p>13:00 Lunch</p> <p>14:15 Tutorial 3: Simulating Cyber-Physical Systems Using SysML and Numerical Simulation Tools <i>Eldad Palachi, IBM Rational</i></p> <p>15:45 Break</p> <p>16:00 Tutorial 4: Improving Verification Productivity with the Dynamic Load and Reseed Methodology <i>Marat Teplitsky, Cadence</i></p> <p>17:30 End of Tutorials</p>
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