



Haifa Verification Conference 2010 – Program

HVC 2010 – Day 1

October 5, 2010

<p>9:00 Registration</p> <p>9:30 Opening Remarks <i>Oded Cohn, Director, IBM Research - Haifa</i></p> <p>10:00 Special Session in Memory of Amir Pnueli <i>Session Chair: Martin Golumbic, University of Haifa</i></p> <p>Personal Remarks <i>David Harel, Weizmann Institute of Science</i></p> <p>Temporal Logic and Model Checking <i>Orna Grumberg, Technion</i></p> <p>11:00 Break</p> <p>11:30 Parameterized System Verification <i>Lenore Zuck, University of Illinois</i></p> <p>Synthesis of Temporal Logic <i>Roderick Bloem, Graz University of Technology</i></p> <p>13:00 Lunch</p> <p>14:30 Invited Talk: (How) Did You Specify Your Test Suite? <i>Helmut Veith, University of Vienna</i></p> <p><i>Session Chair: Alan Hu</i></p>	<p>15:30 Paper Session I: Hardware Simulation <i>Session Chair: Alan Hu</i></p> <p>Feedback-based Coverage-Directed Test Generation: An Industrial Evaluation <i>Charalambos Ioannides, Geoff Barrett, and Kerstin Eder</i></p> <p>Reaching Coverage Closure in Post-Silicon Validation <i>Allon Adir, Amir Nahir, Avi Ziv, Charles Meissner, and John Schumann</i></p> <p>16:30 Break</p> <p>17:00 Paper Session II: Hardware Simulation <i>Session Chair: Eyal Bin</i></p> <p>vlogsl: A Strategy Language for Simulation-based Verification of Hardware <i>Michael Katelman and Jose Meseguer</i></p> <p>Advances in Simultaneous Multithreading Testcase Generation Methods <i>John Ludden, Michal Rimon, Bryan Hickerson, and Allon Adir</i></p> <p>18:00 Reception and Students Event</p>
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