



Haifa Verification Conference – Tutorials

October 27, 2008

09:30-10:00 Registration, Coffee	13:30-14:30 Lunch
10:00-11:30 Verification Challenges of Low-Power Design <i>Karen Yorav, IBM HRL</i>	14:30-16:00 Design and Verification Aspects of Soft Errors <i>Ilan Beer, IBM HRL</i>
11:30-12:00 Break	16:00-16:30 Break
12:00-13:30 When Multicore is Not Just About Architecture and Silicon Technology <i>Vladimir Tsymbal, Intel Corp</i>	16:30-18:00 Moore's Law v. Verification Complexity <i>Jason Baumgartner, IBM US</i>

