



Third Haifa Verification Conference – Call for Papers

Keynote Speakers

Bob Bentley
Aarti Gupta

Invited Speakers

Alan Hu
Robert Kurshan
Wolfgang Roesner
Andreas Zeller

Program committee

Sharon Barner
Lyes Benalycherif
Eyal Bin
Roderick Bloem
Jong-Deok Choi
Alessandro Cimatti
Kerstin Eder
Bernd Finkbeiner
Limor Fix
Laurent Fournier
Orna Grumberg
Aarti Gupta
Klaus Havelund
Alan Hu
Warren Hunt
Daniel Kroening
Tsvi Kuflik
Orna Kupferman
Mark Last
Joao Lourenco
Sharad Malik
Erich Marschner
Ken McMillan
Amos Noy
Viresh Paruthi
Carl Pixley
Andrew Piziali
Wolfgang Roesner
Padmanabhan (Peter)
Santhanam
Fabio Somenzi
Scott D. Stoller
Ofer Strichman
Paul Strooper
Serdar Tasiran
Shmuel Ur
Willem Visser
Tao Xie
Karen Yorav (chair)
Avi Ziv



October 23 – 25, 2007

Sponsored by the IBM Research Lab in Haifa and

Caesarea Rothschild Institute at the University of Haifa

<http://www.haifa.il.ibm.com/Workshops/verification2007/>

HVC 2007 is the third annual conference dedicated to advancing the verification and testing of both hardware and software. The conference provides a forum for academia, industry, and the research and development community to share their work, exchange ideas, and discuss the challenges and future directions in the field.

Verification methods generally divide into two paradigms – formal verification and dynamic verification (testing). Within each paradigm, different techniques are used for hardware and software systems. Yet all are aimed at the same goal of ensuring the correct functionality of a complicated system. HVC is the only conference that brings together researchers from all verification fields, thereby encouraging the migration of methods and ideas between domains. To this end, we are announcing a *special interest in hybrid methods* for this year's conference. We encourage the submission of works that exemplify the successful integration of ideas from different domains of expertise.

Accepted papers will be presented at the conference and published in a post-conference proceedings in Springer's Lecture Notes in Computer Science series (LNCS). The top 10%-20% papers will be invited to submit journal versions of their work to a special edited issue of STTT (<http://sttt.cs.uni-dortmund.de/>).

Topics of interest include, but are not limited to:

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| Simulation-based verification | Formal verification methods |
| High-level stimuli generation | Model checking |
| Equivalence checking | SAT-based verification algorithms |
| Classification of hardware bugs | Static analysis |
| Design for verifiability | Concurrency testing |
| Hardware/software co-verification | Debugging |
| Emulation and acceleration | Defect prevention |
| CSP applications in functional verification | Test-driven development |
| Hybrid verification methods | Model-based testing |
| Verification coverage | Developer testing |
| Formal Specification Languages | Review and inspection |
| Semi-formal verification | Pair testing and first testing |
| Test automation frameworks | Automatic test generation |

Authors are invited to submit papers describing original, unpublished work. Papers are to be submitted electronically through the conference web page, in LNCS format (<http://www.springer.com/east/home/computer/lncs>). The page limit is fifteen (15) for regular papers and five (5) for tool papers. Simultaneous submission to other conferences is strictly forbidden.

Important dates:

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| Paper submission deadline: | July 05, 2007 |
| Notification of acceptance: | August 15, 2007 |
| Conference gathering: | October 23 – 25, 2007 |
| Final version of accepted papers: | November 19, 2007 |