



IBM Haifa Verification Conference 2006 – Agenda

Day 1: Hardware Verification

October 23, 2006

<p>9:30 Welcome <i>Eyal Bin, Program Chair, IBM Haifa Research Lab; David Bernstein, Manager, Software and Verification, IBM Haifa Research Lab; Irith Hartman, Scientific Manager, Caesarea Rothschild Institute</i></p> <hr/> <p>Property Specification Session chair: Karen Yorav, IBM Haifa Labs</p> <p>10:00 Invited speaker: Sweet and Sour: Adventures in the Development of a Property Specification Language <i>Cindy Eisner, IBM Haifa Research Lab</i></p> <p>10:45 Model Checking PSL Using HOL and SMV <i>Thomas Tuerk, Klaus Schneider, and Mike Gordon, University of Kaiserslautern, Germany / University of Cambridge, UK</i></p> <p>11:15 Speed Networking session and coffee break</p> <hr/> <p>Dynamic Verification and Testing Session chair: Laurent Fournier, IBM Haifa Labs</p> <p>12:00 Using Linear Programming Techniques for Scheduling-based Random Test-case Generation <i>Amir Nahir, Yossi Shiloach, and Avi Ziv, IBM Haifa Research Lab</i></p> <p>12:30 Extracting a Simplified View of Design Functionality Based on Vector Simulation <i>Onur Guzey, Hung-Pin (Charles) Wen, Li-C. Wang, Tao Feng, Hillel Miller and Magdy Abadir, University of California Santa Barbara, Cadence, Freescale Semiconductor</i></p>	<p>13:00 Reusable On-Chip System Level Verification for Simulation Emulation and Silicon <i>Avishay Maman, Sharon Goldschlager, Hillel Miller, David Bell, Rob Slater, Oded Bin-Moshe, Nissan Levi, and Hagit Gilboa, Freescale Semiconductor Israel</i></p> <p>13:30 Lunch</p> <hr/> <p>Formal Verification Session chair: Yaron Wolfsthal, IBM Haifa Labs</p> <p>15:00 Keynote: System Modeling and Formal Verification with UCLID <i>Randal E. Bryant, Dean and University Professor, School of Computer Science, Carnegie Mellon University</i></p> <p>16:00 Automatic Fault Localization for Property Checking <i>Stefan Staber, Görschwin Fey, Roderick Bloem, and Rolf Drechsler, Graz University of Technology, Austria / University of Bremen, Germany</i></p> <p>16:30 Verification of Data Paths Using Unbounded Integers: Automata Strike Back <i>Alexander Tobias Schuele and Klaus Schneider, University of Kaiserslautern, Germany</i></p> <p>17:00 Closing remarks <i>Meir Nissensohn, General Manager, IBM Israel</i></p> <p>17:30 Cocktail Party</p>
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