



## *Invitation to IBM's Verification Seminar 2002*

September 12, 2002

Organized by IBM Haifa Research Labs, Israel

[www.haifa.il.ibm.com/Workshops/verification2002](http://www.haifa.il.ibm.com/Workshops/verification2002)

The Verification Technologies Department at the IBM Haifa Research Lab (HRL) cordially invites you to a full-day seminar on Verification, to be held on Thursday, the 12<sup>th</sup> of September 2002.

This is the third in a sequence of verification seminars held by HRL since 1999.

The seminar will take place at HRL's new site on the Haifa University campus, HRL's auditorium (room L100), from 9:30 to 17:15. Lunch and light refreshments will be provided. Participation is free.

Please confirm your participation by September 5 via the seminar website:

[www.haifa.il.ibm.com/Workshops/verification2002](http://www.haifa.il.ibm.com/Workshops/verification2002).

### **9:15 Arrival**

#### **9:30 Welcome**

Yossi Malka, Mgr., Verification Technologies, IBM Haifa Labs

#### **9:45 Formal Specification Using Sugar 2.0**

Cindy Eisner, IBM Haifa Labs

#### **10:10 Distributed Symbolic Model Checking**

Tamir Heyman, Technion

#### **10:40 Full Cycle Functional Coverage: Coverage Success Stories**

Avi Ziv, IBM Haifa Labs

### **11:05 Break**

#### **11:20 Coverage Directed Test Generation for Functional Verification Using Bayesian Networks**

Shai Fine, IBM Haifa Labs

#### **11:50 Automatic Discovery of Mutual Exclusion Algorithms**

Yoah Bar-David and Gadi Taubenfeld (speaker), Open University & IDC

#### **12:20 Verifying the Discovery (TM) System**

*Controllers Family - A Case Study*

Kobi Pines, Galileo Technology

### **12:50 Lunch**

#### **13:50 Keynote: From Aerospace to C. Elegans: Modeling and Analyzing the Behavior of Complex Reactive Systems**

David Harel, Weizmann Institute of Science

#### **14:40 Improved Abstraction/Refinement Verification Algorithms with Backtracking and Layering**

Sharon Barner, IBM Haifa Labs

#### **15:10 Information-flow Models for Shared Memory**

Hagit Attiya, Technion

### **15:40 Break**

#### **16:00 What's Between Constraint Satisfaction and Random Test Program Generation**

Roy Emek, IBM Haifa Labs

#### **16:30 Banias - Design and Verification of a Mobile-specific CPU**

Ron Friedman, Intel

#### **17:00 Concluding Remarks**

Michael Rodeh, Director, IBM Haifa Labs

### **ORGANIZING COMMITTEE**

Moshe Levinger, IBM HRL

Eyal Bin, IBM HRL

Yaron Wolfsthal, IBM HRL